



**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Norio IMAOKA

Application No.: 10/807,438

Docket No.: 118868

Filed: March 24, 2004

For: SEMICONDUCTOR CHIP, SEMICONDUCTOR DEVICE, METHOD OF  
MANUFACTURING THE SAME, CIRCUIT BOARD, AND ELECTRONIC  
EQUIPMENT

**REQUEST FOR CORRECTION OF PALM RECORDS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which an error has been corrected in red. This error is being brought to the attention of the Patent and Trademark Office so that it may correct its records. A Supplemental Application Data Sheet is attached.

Respectfully submitted,

James A. Oliff  
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JAO:MB/gam

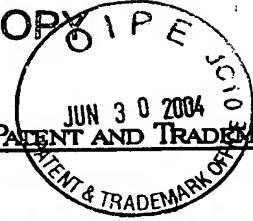
Date: June 30, 2004

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APPL NO.	FILING OR 371 (C) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/807,438	03/24/2004	2811	2120	118868	11	52	12

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CONFIRMATION NO. 4268

## FILING RECEIPT



\*OC000000012873113\*

Date Mailed: 06/04/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

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## Assignment For Published Patent Application

SEIKO EPSON CORPORATION, Tokyo, JAPAN;

## Domestic Priority data as claimed by applicant

## Foreign Applications

JAPAN 2003-088221 03/27/2003

If Required, Foreign Filing License Granted: 06/04/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

## Title

Semiconductor chip, semiconductor device, method of manufacturing the same, circuit board, and electronic equipment